

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 249/236 DIV

In re the Application of

Bong-seok KIM

Art Unit:

Serial No.

Examiner

:

*[Division of Serial No. 09/850,143, filed May 8, 2001]*

Filed: July 22, 2003

Confirmation No.

For: MERGED MEMORY AND LOGIC  
SEMICONDUCTOR DEVICE OF SALICIDED DUAL  
GATE STRUCTURE INCLUDING EMBEDDED  
MEMORY OF SELF-ALIGNED CONTACT  
STRUCTURE AND MANUFACTURING METHOD  
THEREOF

**CONFIRMATION**  
**CLAIM FOR CONVENTION PRIORITY**

Commissioner for Patents  
Alexandria, VA 22313-1450

Sir:

The benefit of the filing date of the following prior foreign application filed in the following foreign country is hereby requested, and the right of priority provided in 35 U.S.C. §119 is hereby claimed:

**Application No. 00-29302**

**- filed May 30, 2000**


**- Republic of Korea**

In support of this claim, a certified copy of said original foreign application was filed in parent application Serial No. 09/850,143 on May 8, 2001.

Acknowledgment of applicant's claim for priority and receipt of the certified priority document is solicited.

Respectfully submitted,

Date: July 22, 2003

  
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